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	Application Number		10715459		
	Filing Date		2003-11-19		
INFORMATION DISCLOSURE	First Named Inventor	Curtis	Curtis Davis		
STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Art Unit		2183		
(Not for Submission under 57 Of K 1.55)	Examiner Name	Keith	E. Vicary		
	Attorney Docket Numb	er	NVDA/AG-08-0112-US		

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	1	SUZU MPEC	IAZU et al. "A Microprocesso G-2 Decoder," IEEE Journal	or with a 128-Bit CPU, Ten lof Solid State Circuits, Vol.	Floating 34, No	g MAC's, Four Floating-Povember 1999, pp. 1608-16	oint Dividers, and an 618.	
	2	Office Action, Ser. No. 10/715,400, dated February 16, 2010.						
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